METHOD AND SYSTEM FOR INTEGRATED CIRCUIT BACKSIDE NAVIGATION

ABSTRACT OF THE DISCLOSURE

The backside navigation method of the present invention includes milling a fiducial opening through the substrate of an integrated circuit. The milling process is stopped when the fiducial opening reaches the bottom of a trench isolation structure. The trench isolation structure delineated by the fiducial opening may be imaged and registered to a computer aided design layout image to achieve sub-micron navigation resolution.

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